

Fig.1

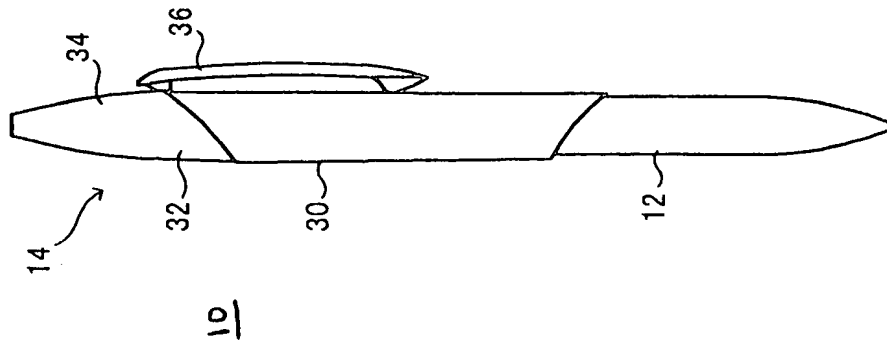


Fig.2

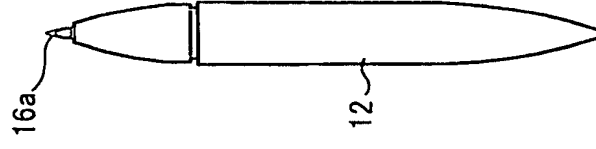


Fig.3

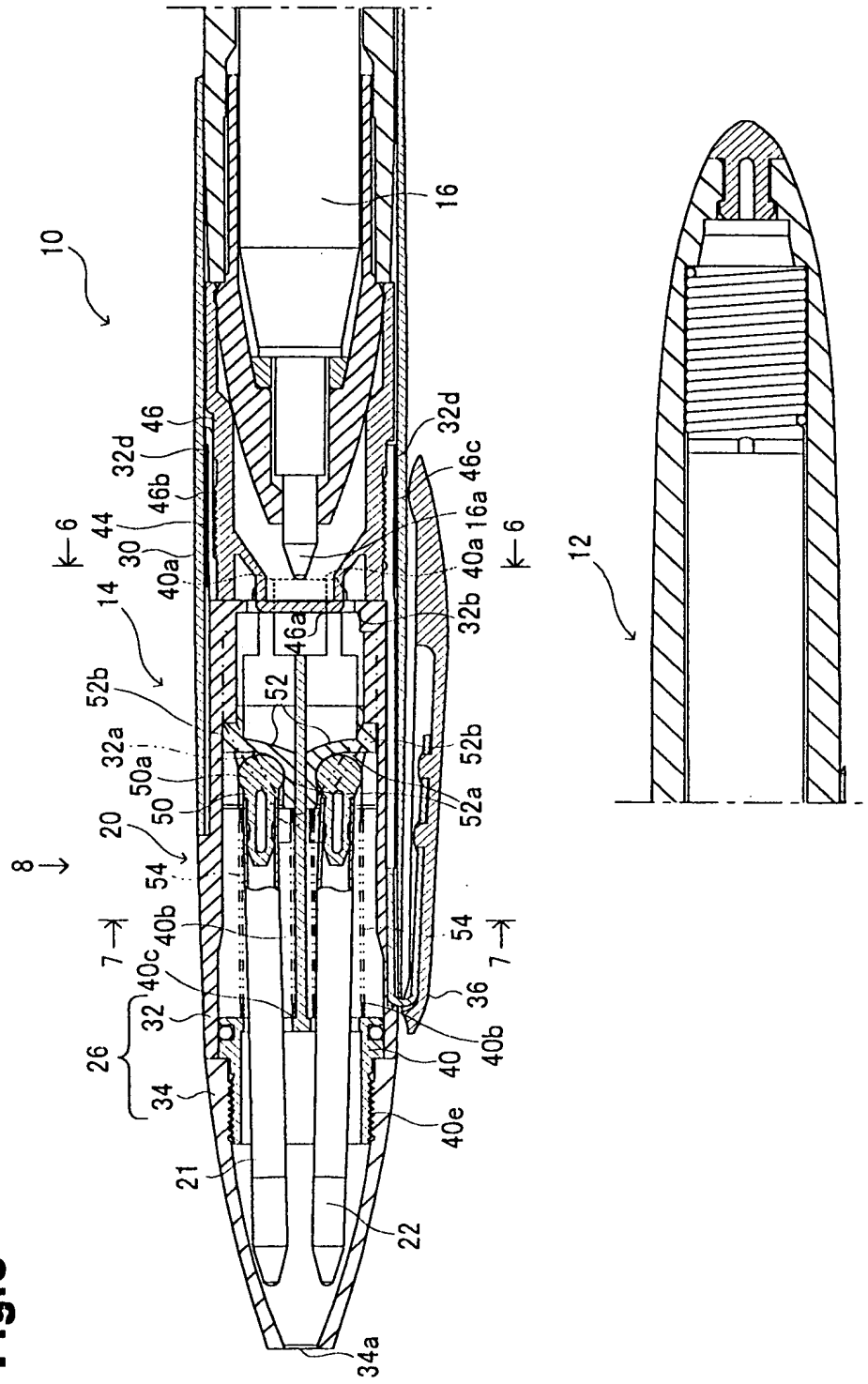




Fig. 5

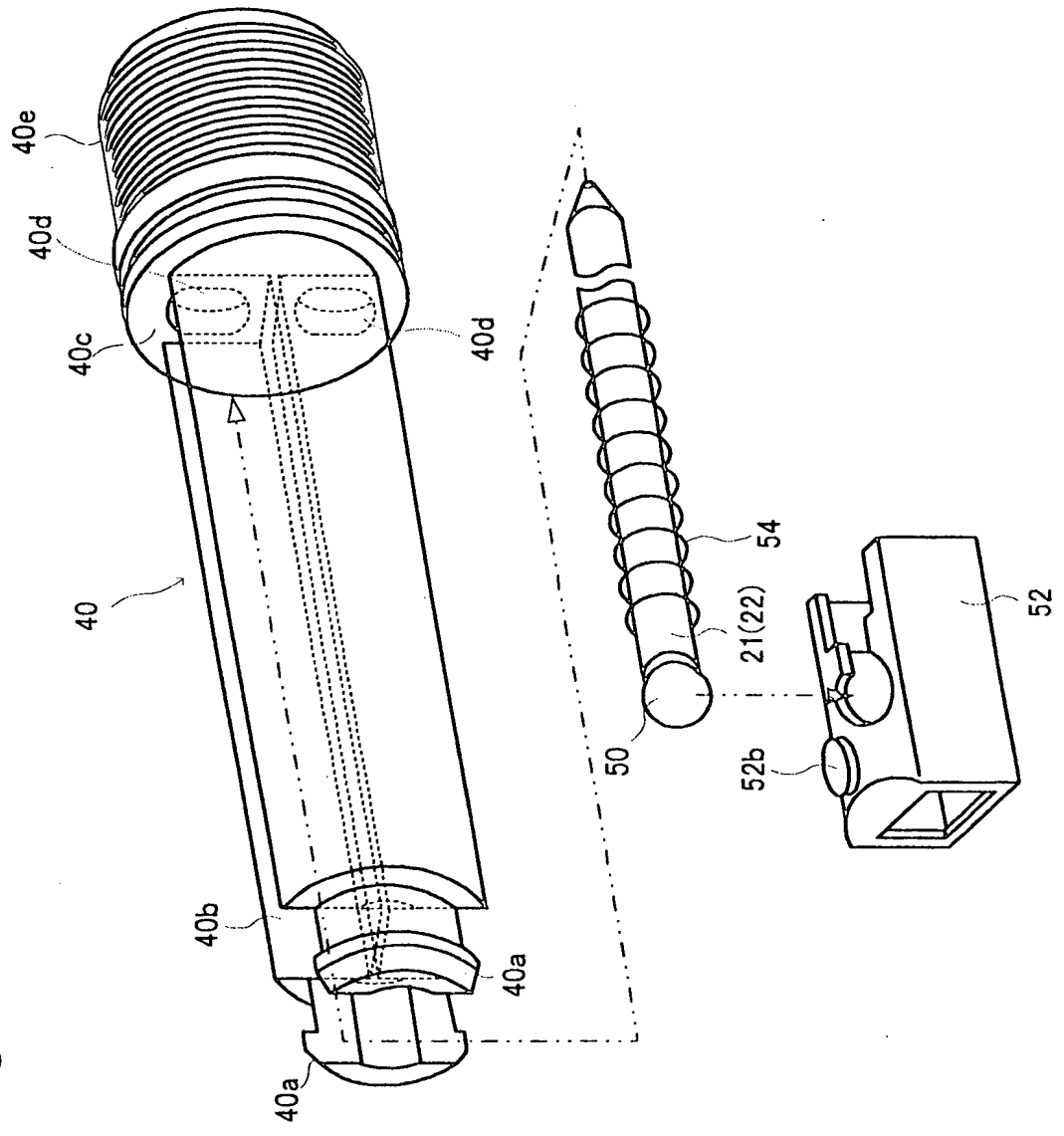


Fig.6

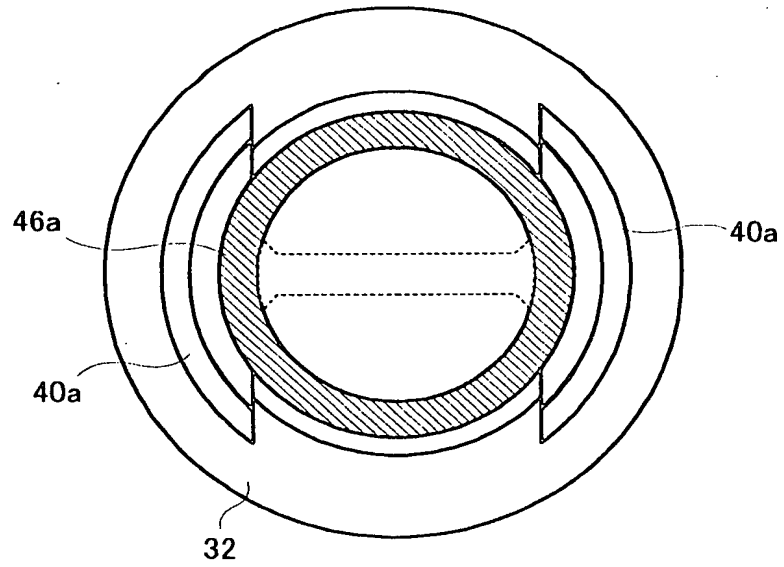


Fig.7

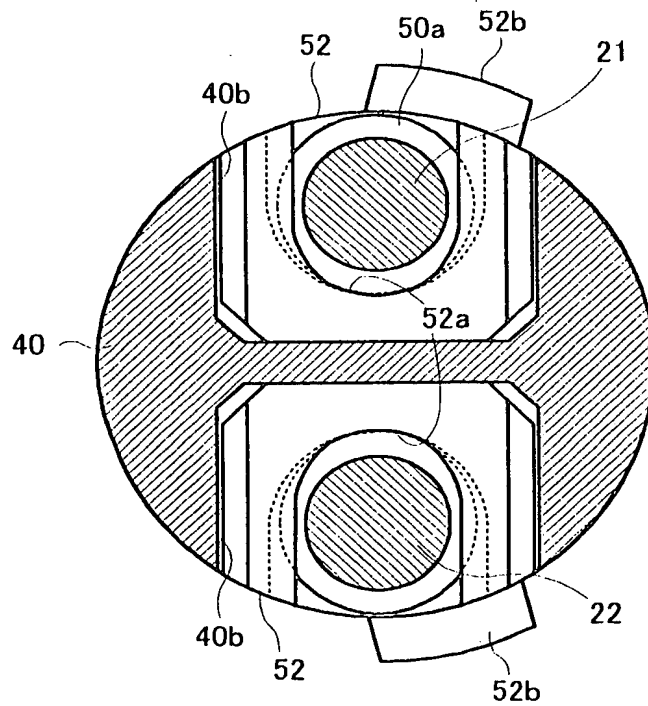


Fig.8

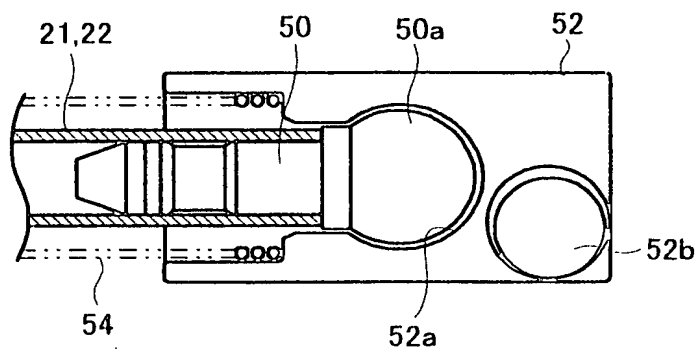


Fig.9

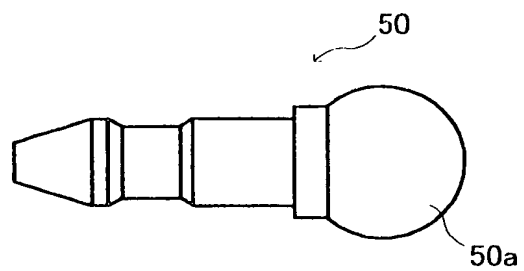


Fig. 10(a)

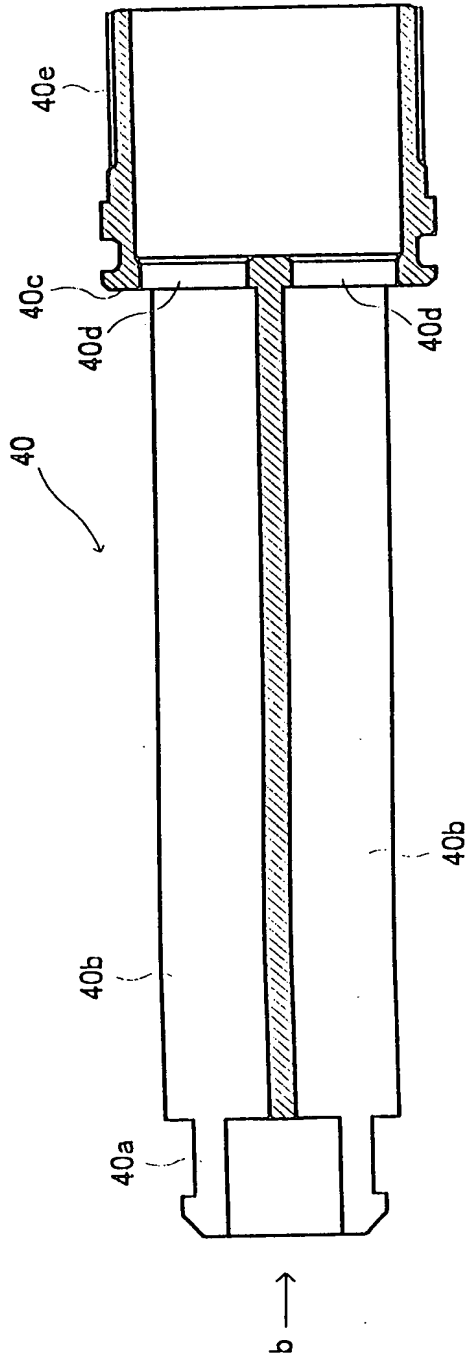


Fig. 10(b)

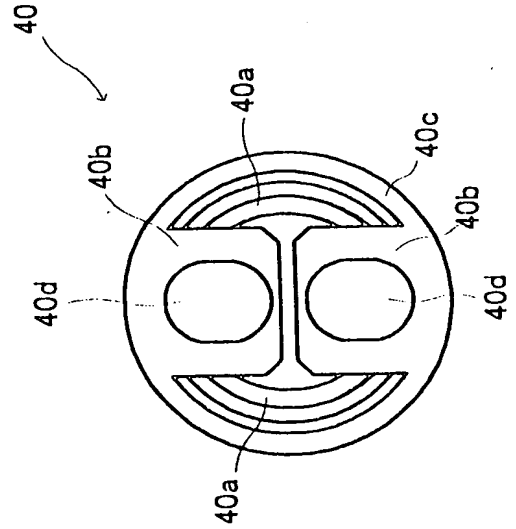


Fig.11

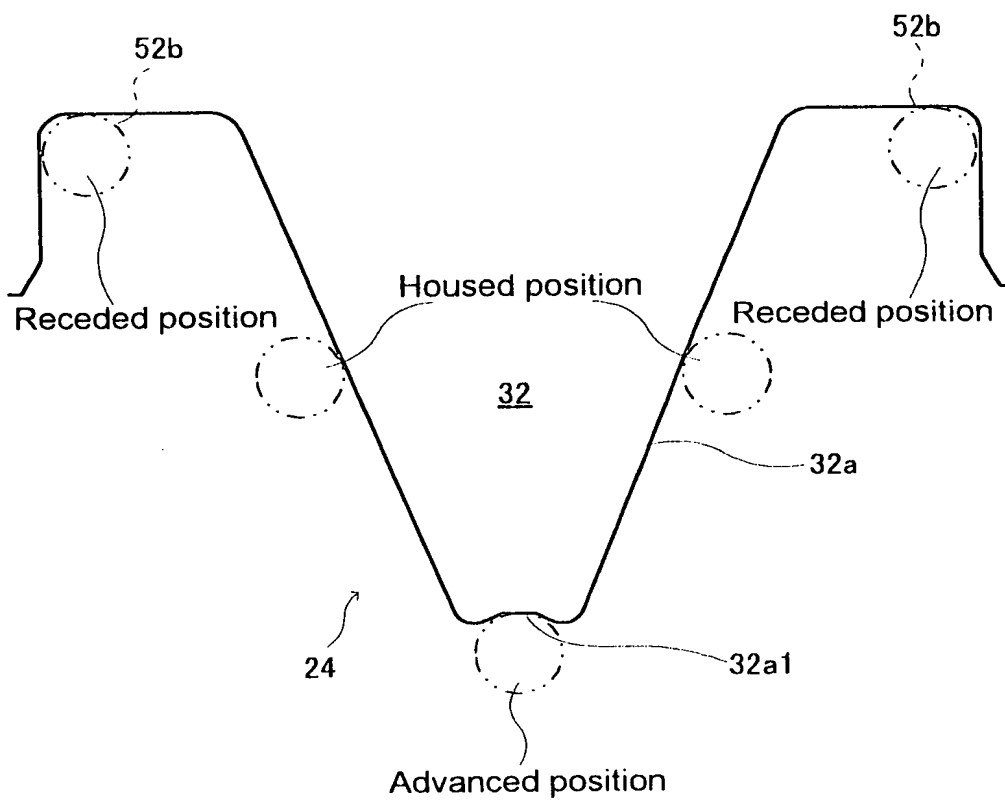




Fig.12

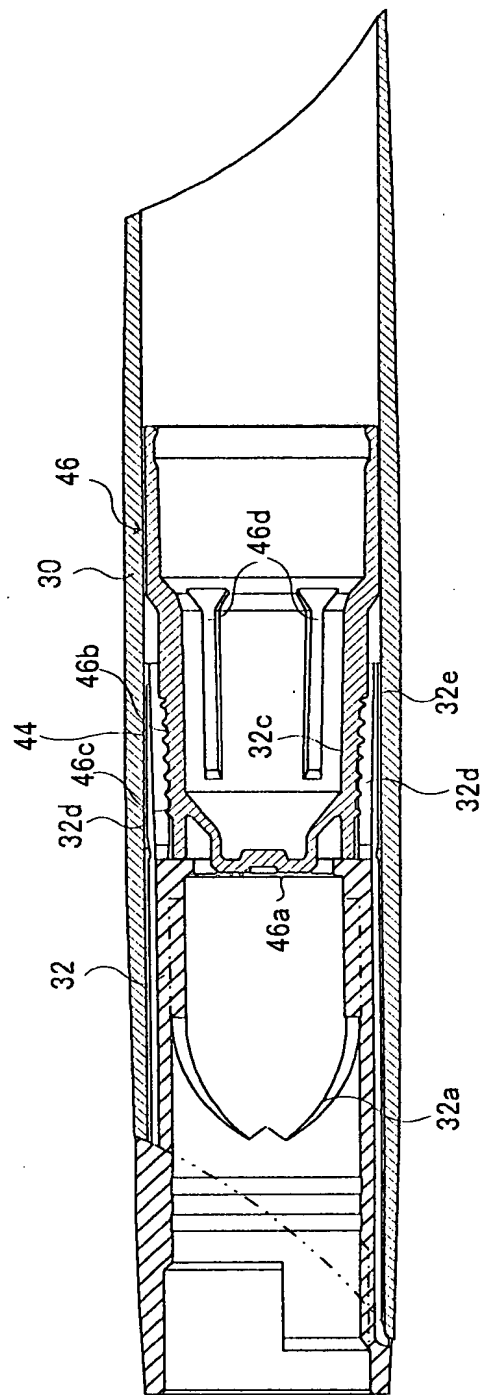


Fig. 13(a)

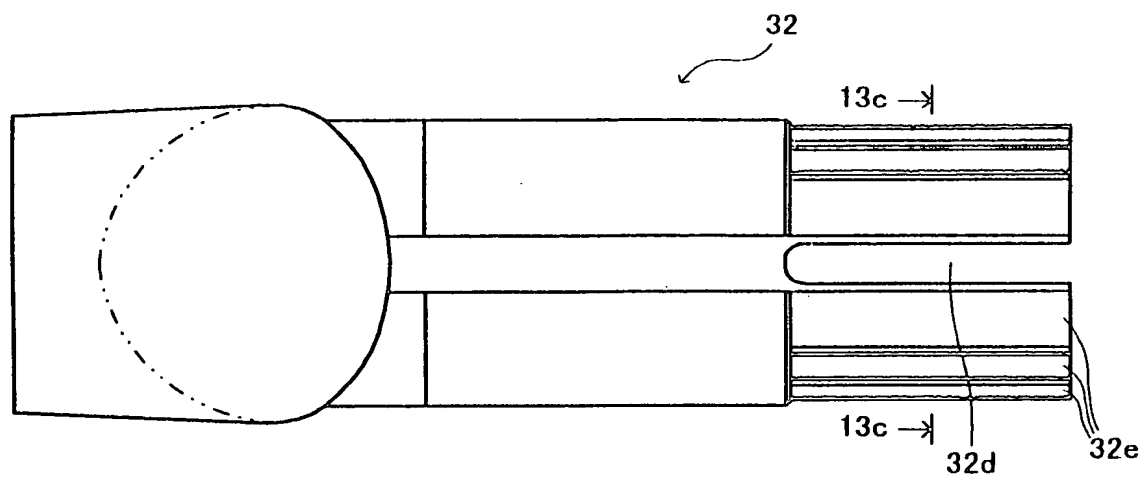


Fig. 13(b)

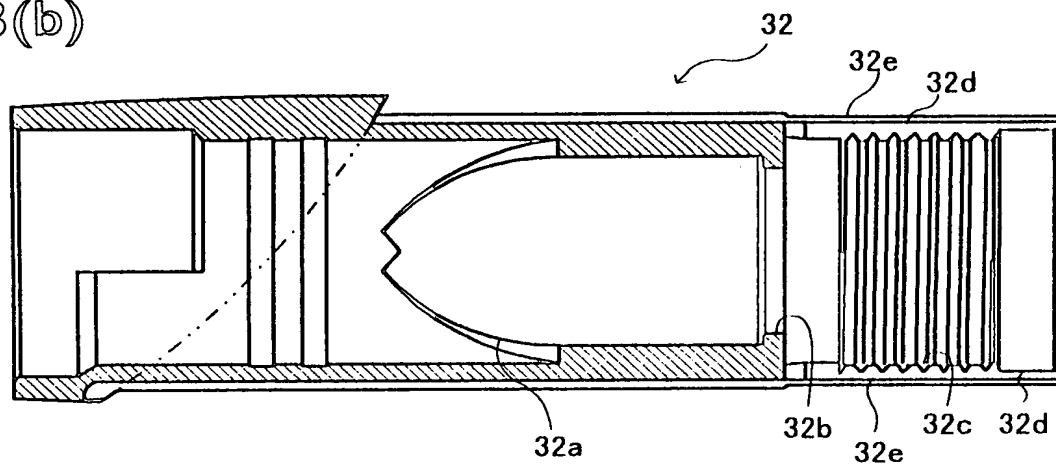


Fig. 13(c)

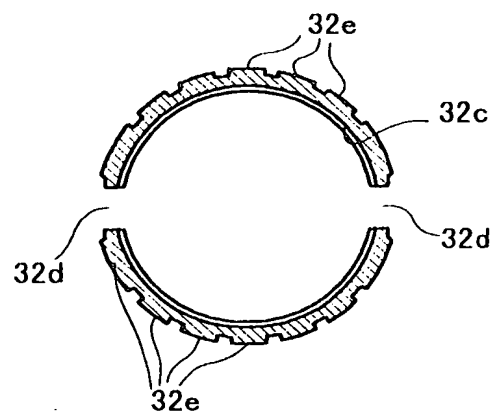


Fig. 14(a)

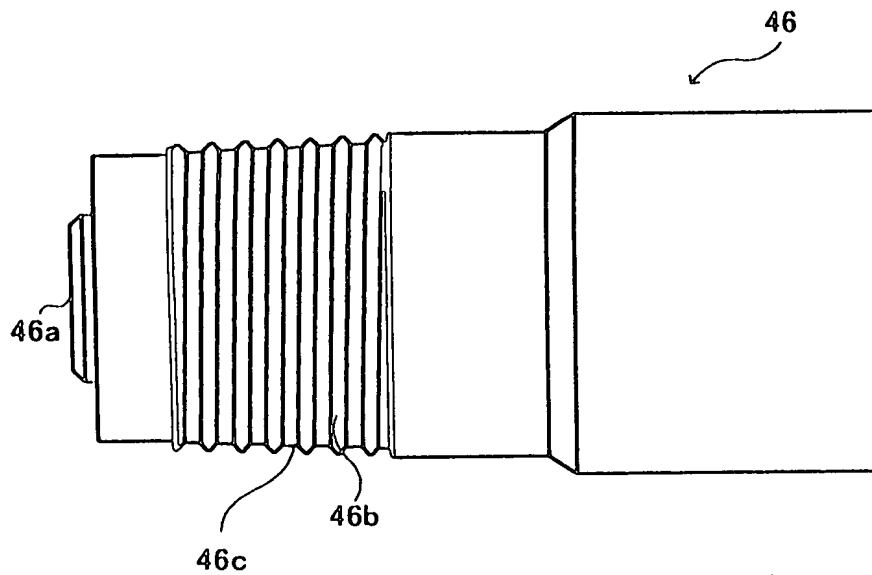


Fig. 14(b)

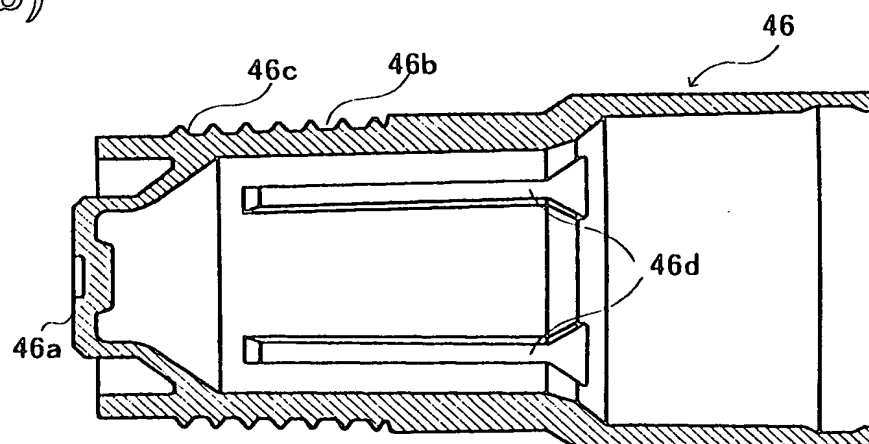


Fig. 15

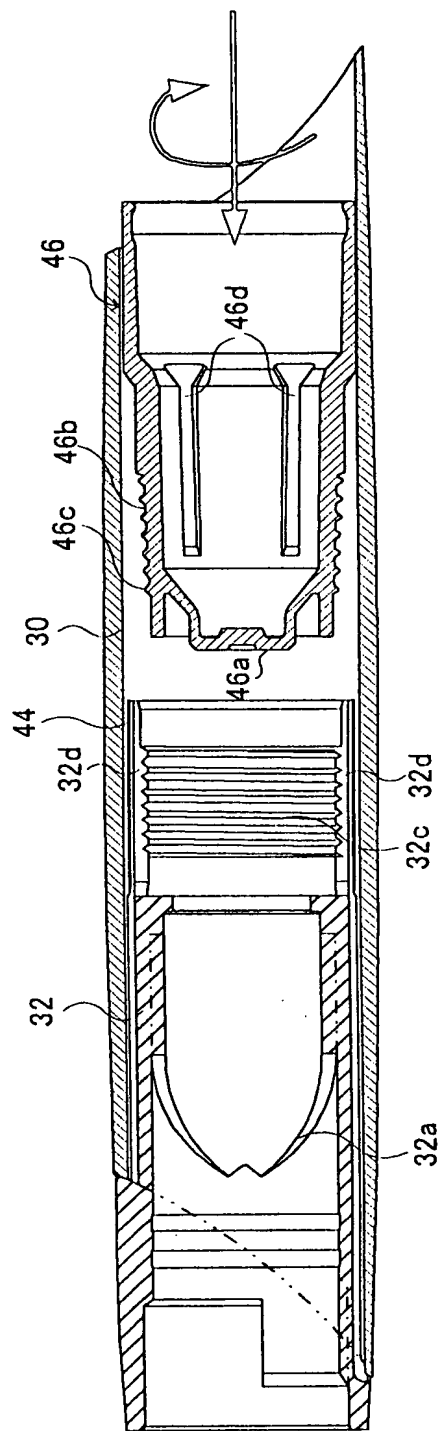


Fig. 16

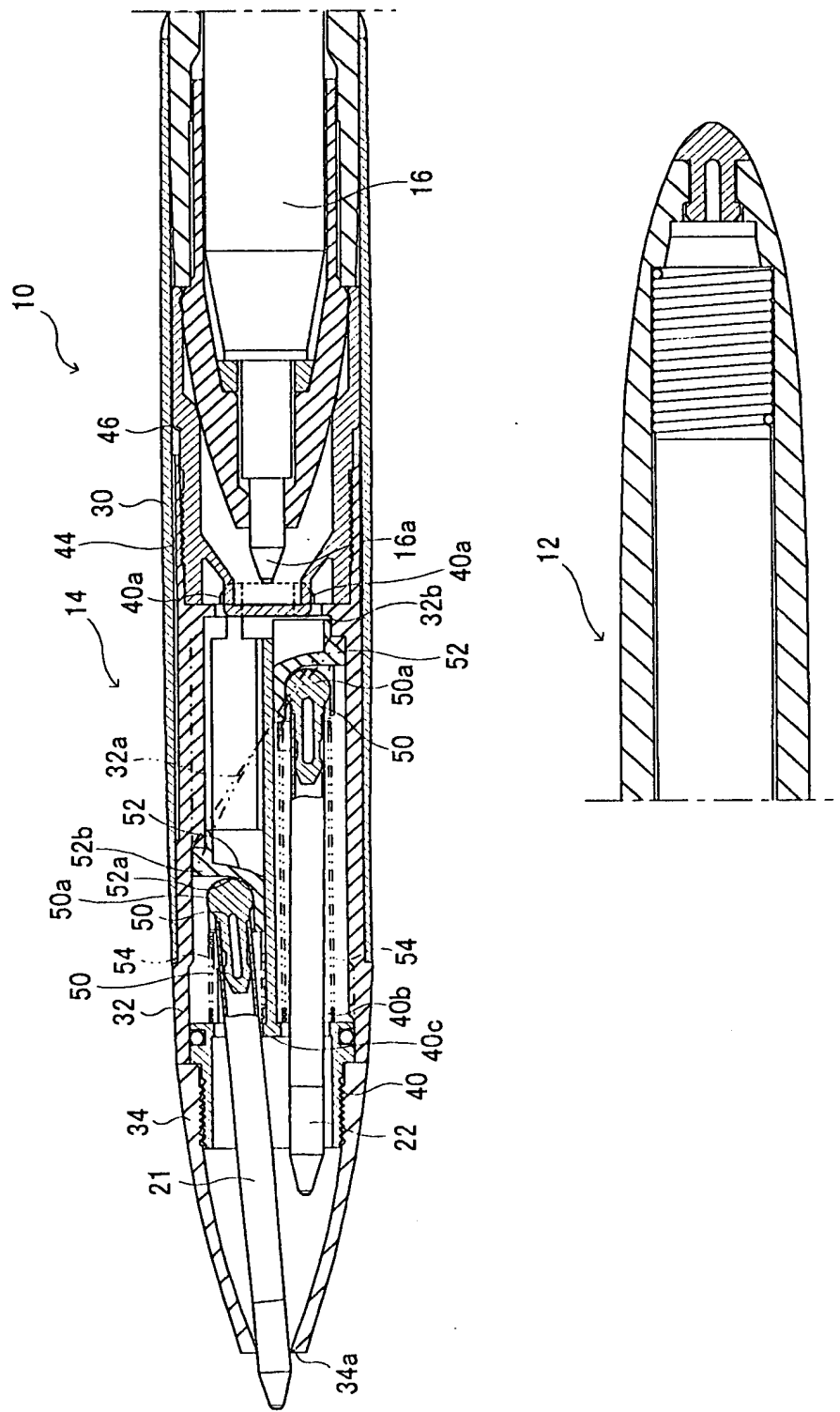


Fig. 17

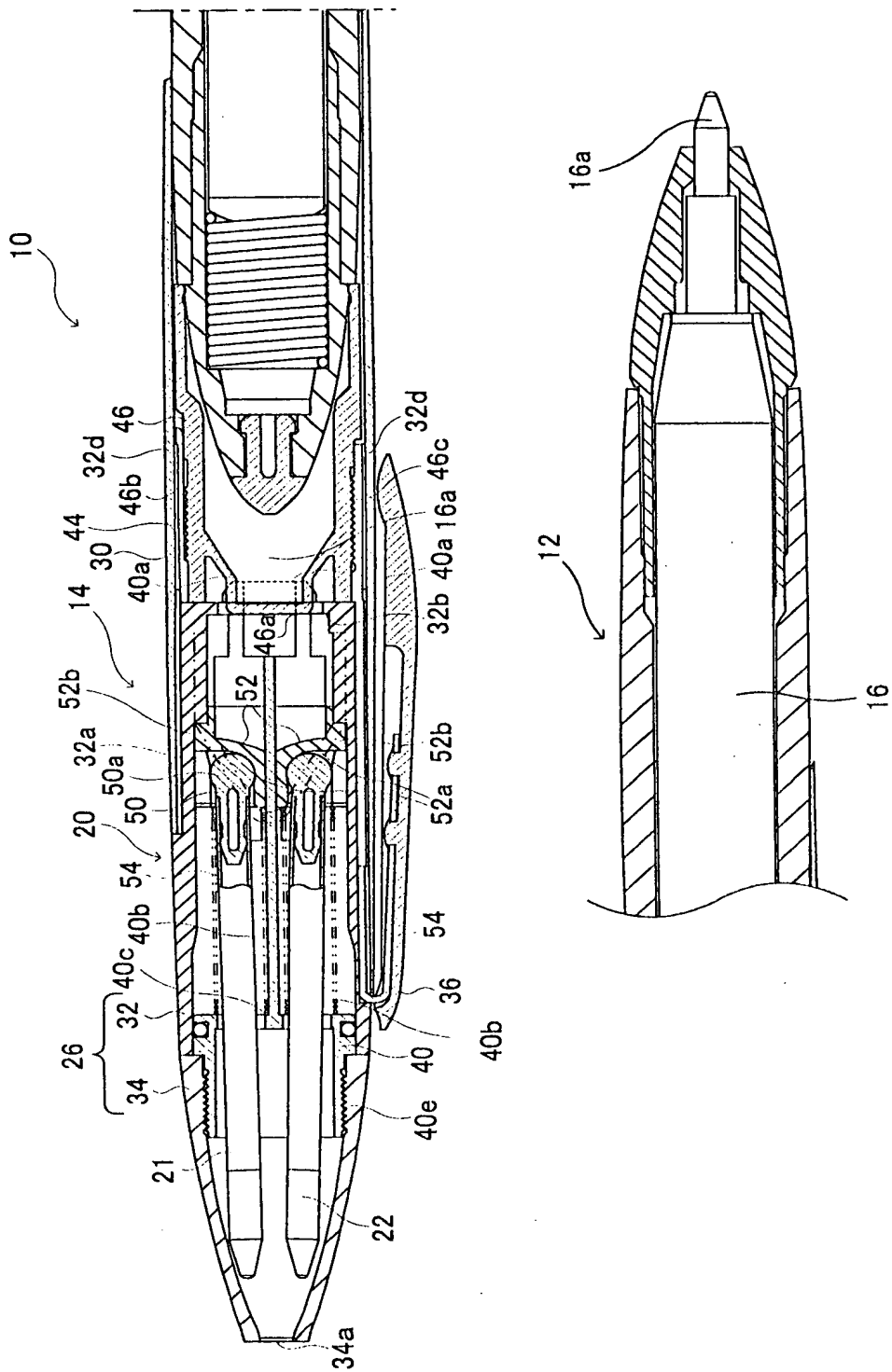


Fig.18

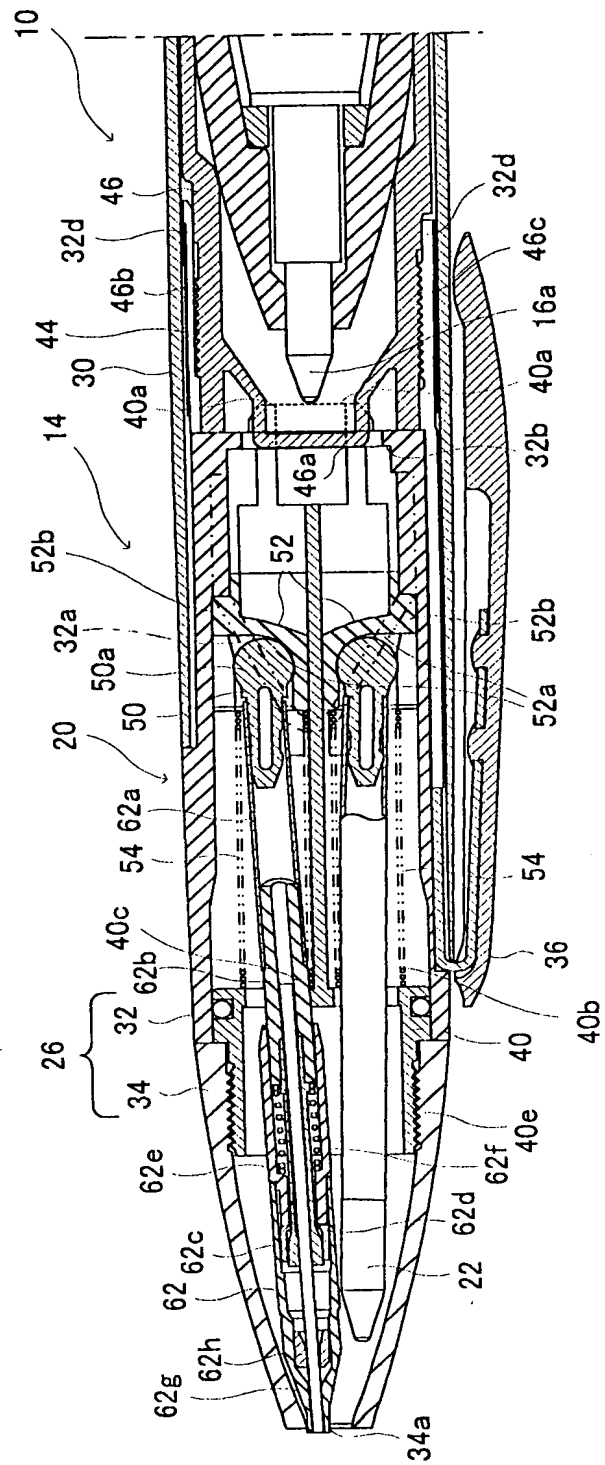






Fig.20

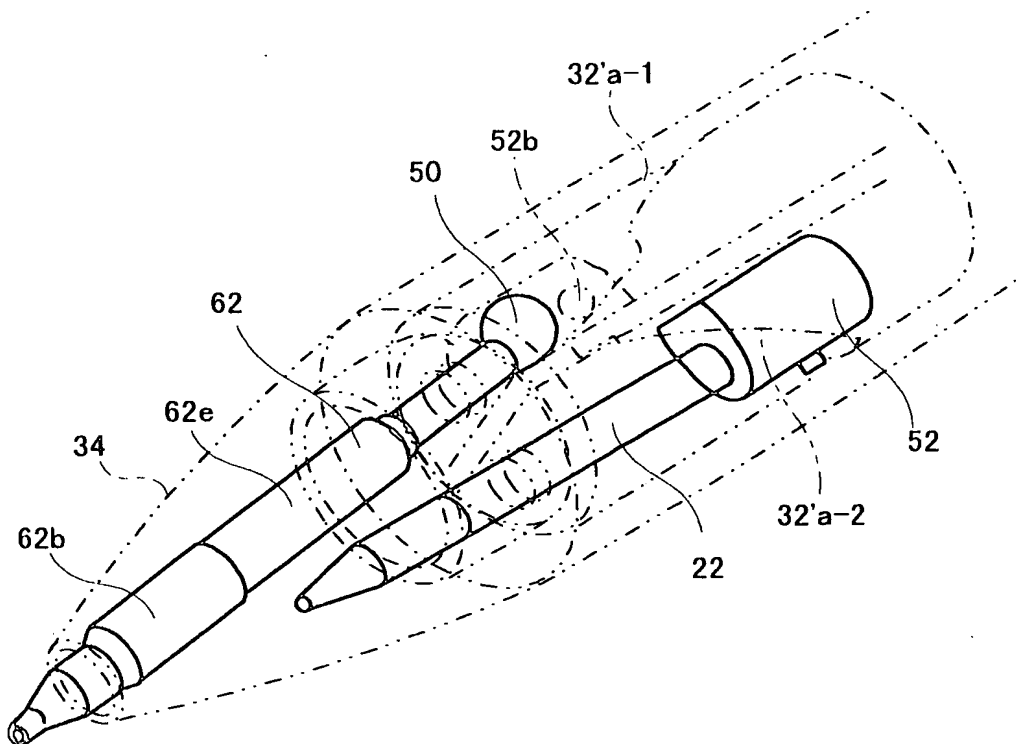




Fig. 22

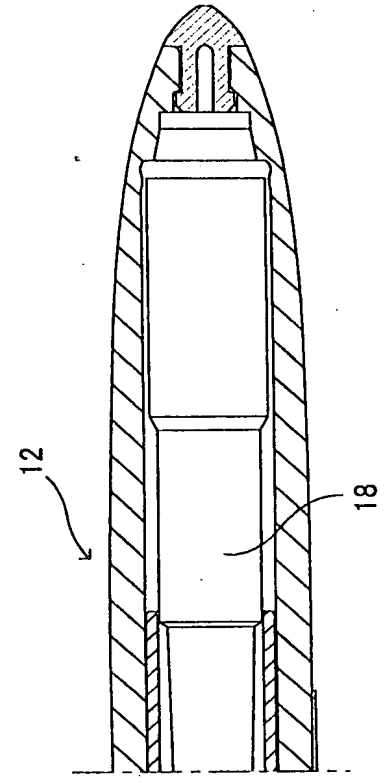
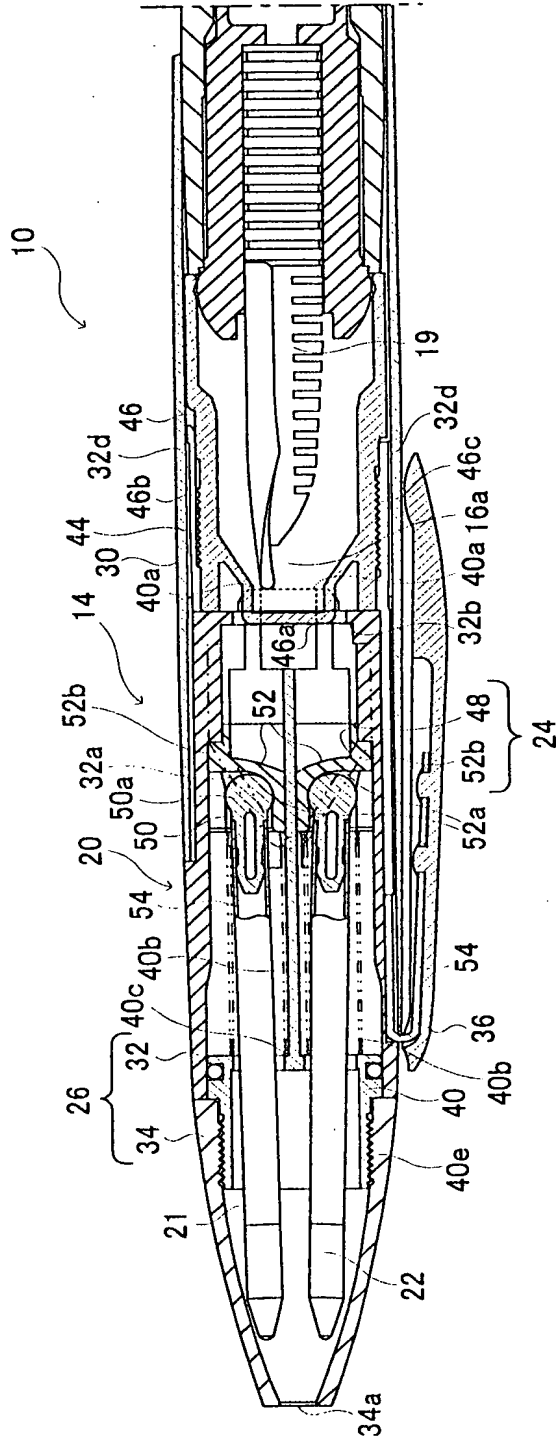


FIG. 23 is a cross-sectional view of a device in a first state, showing a first set of components 30, 32-1, 46-1, 46b, 46d, 48, 46e, 32c, 46a, and 32a.

Fig. 23

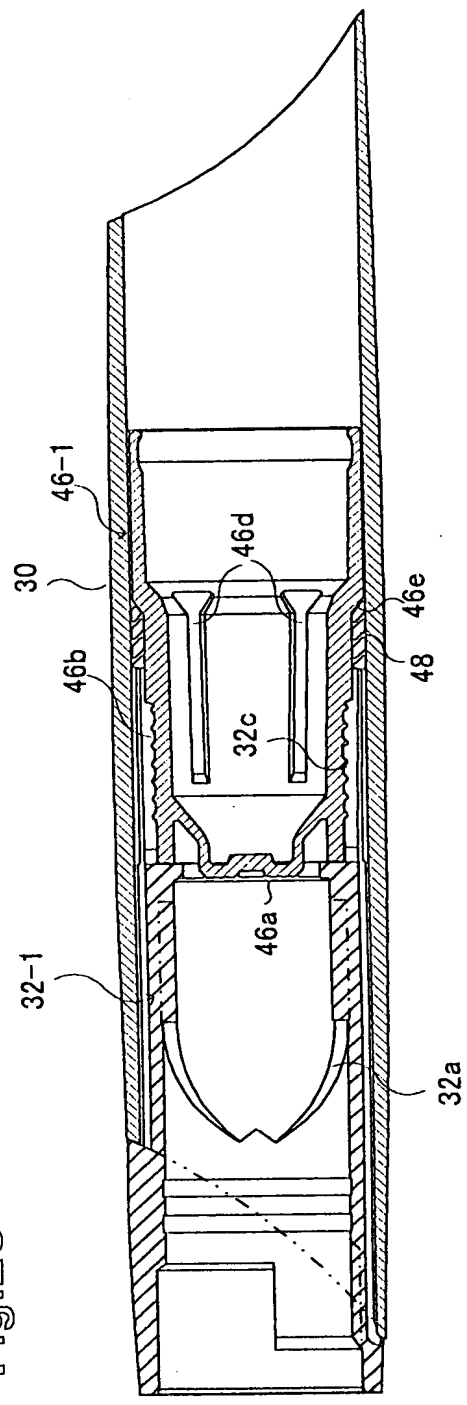


Fig. 24

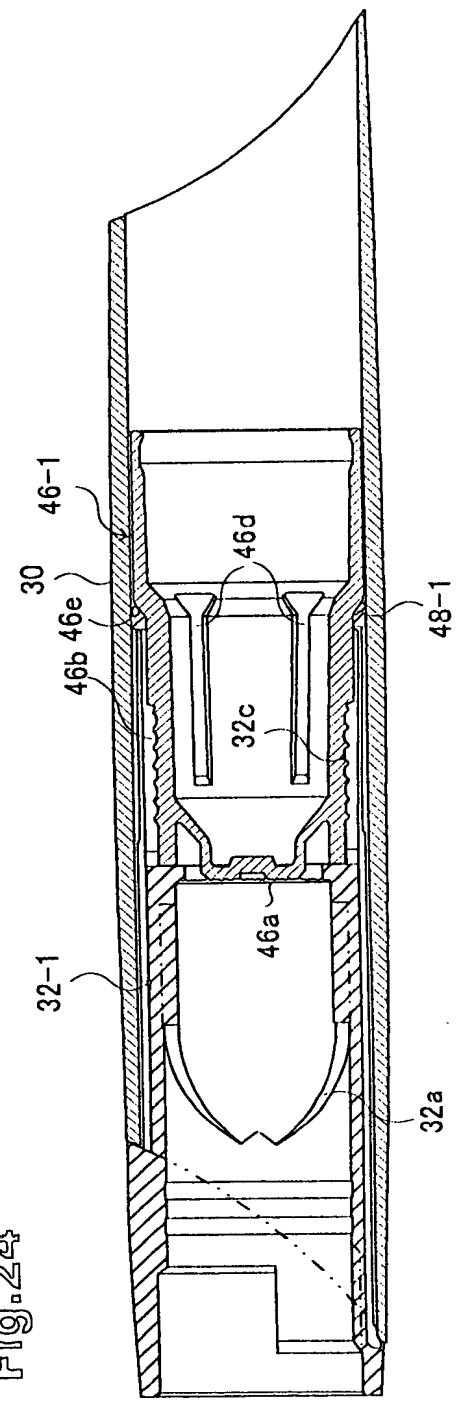


FIG. 25 is a cross-sectional view of a device in a closed position.

Fig. 25

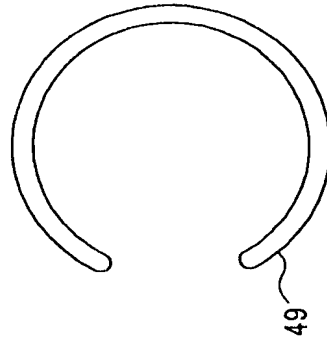
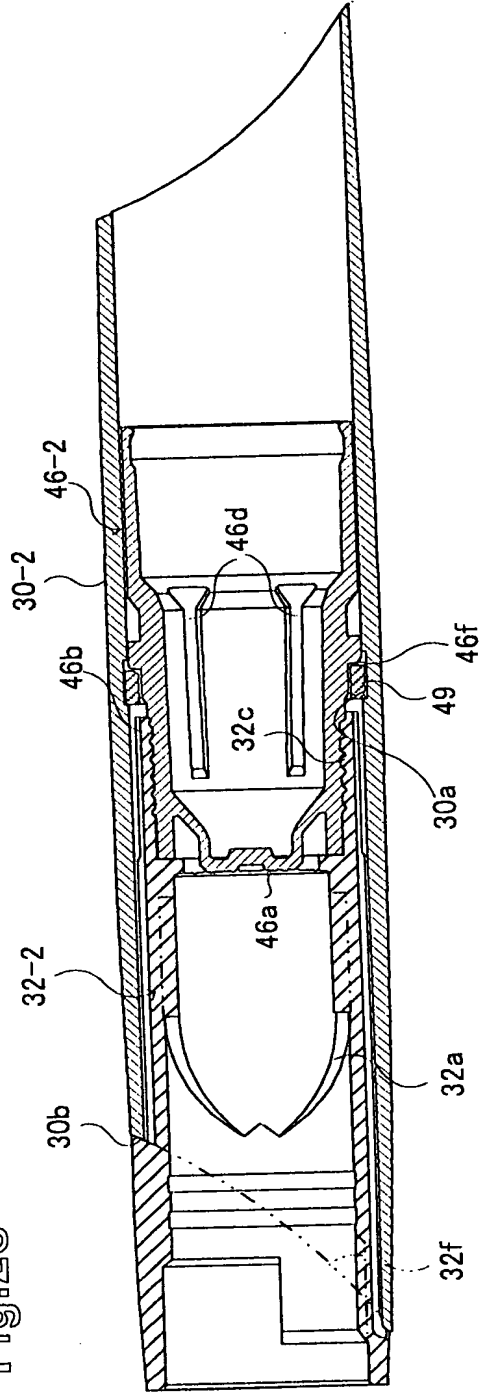


Fig. 26

**Fig. 27**

